

General Description

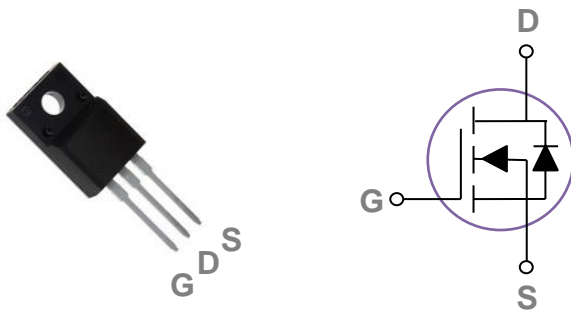
These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	R _{DS(ON)}	I _D
30V	4mΩ	66A

Features

- 30V, 66A, R_{DS(ON)} = 4mΩ @ V_{GS} = 10V
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

TO220F Pin Configuration



Applications

- MB / VGA / Vcore
- POL Applications
- SMPS 2nd SR

Absolute Maximum Ratings T_C=25°C unless otherwise noted

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	30	V
V _{GS}	Gate-Source Voltage	±20	V
I _D	Drain Current – Continuous (T _C =25°C)	66	A
	Drain Current – Continuous (T _C =100°C)	42	A
I _{DM}	Drain Current – Pulsed ¹	264	A
EAS	Single Pulse Avalanche Energy ²	125	mJ
IAS	Single Pulse Avalanche Current ²	50	A
P _D	Power Dissipation (T _C =25°C)	29	W
	Power Dissipation – Derate above 25°C	0.23	W/°C
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction to ambient	---	62	°C/W
R _{θJC}	Thermal Resistance Junction to Case	---	4.3	°C/W

Electrical Characteristics (T_J=25 °C, unless otherwise noted)
Static State Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	30	---	---	V
ΔBV _{DSS} /ΔT _J	BV _{DSS} Temperature Coefficient	Reference to 25°C, I _D =1mA	---	0.03	---	V/°C
I _{DSS}	Drain-Source Leakage Current	V _{DS} =30V, V _{GS} =0V, T _J =25°C	---	---	1	uA
		V _{DS} =24V, V _{GS} =0V, T _J =125°C	---	---	10	uA
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0V	---	---	±100	nA
R _{DS(ON)}	Static Drain-Source On-Resistance ³	V _{GS} =10V, I _D =24A	---	3.4	4	mΩ
		V _{GS} =4.5V, I _D =12A	---	4.3	6	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	1.2	1.6	2.5	V
ΔV _{GS(th)}	V _{GS(th)} Temperature Coefficient		---	-5	---	mV/°C
g _{fs}	Forward Transconductance	V _{DS} =10V, I _D =10A	---	16	---	S

Dynamic Characteristics

Q _g	Total Gate Charge ^{3, 4}	V _{DS} =15V, V _{GS} =10V, I _D =24A	---	53	106	nC
Q _{gs}	Gate-Source Charge ^{3, 4}		---	4.2	8	
Q _{gd}	Gate-Drain Charge ^{3, 4}		---	13	20	
T _{d(on)}	Turn-On Delay Time ^{3, 4}	V _{DD} =15V, V _{GS} =10V, R _G =3.3Ω I _D =15A	---	12.6	24	ns
T _r	Rise Time ^{3, 4}		---	19.5	37	
T _{d(off)}	Turn-Off Delay Time ^{3, 4}		---	42.8	81	
T _f	Fall Time ^{3, 4}		---	13.2	25	
C _{iss}	Input Capacitance	V _{DS} =25V, V _{GS} =0V, F=1MHz	---	2200	3300	pF
C _{oss}	Output Capacitance		---	280	410	
C _{rss}	Reverse Transfer Capacitance		---	177	260	
R _g	Gate resistance	V _{GS} =0V, V _{DS} =0V, F=1MHz	---	2	4	Ω

Drain-Source Diode Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current	V _G =V _D =0V, Force Current	---	---	66	A
I _{SM}	Pulsed Source Current ³		---	---	132	A
V _{SD}	Diode Forward Voltage ³	V _{GS} =0V, I _S =1A, T _J =25°C	---	---	1	V
t _{rr}	Reverse Recovery Time	V _{DS} =30V, I _S =24A, di/dt=100A/μs	---	19.1	---	ns
Q _{rr}	Reverse Recovery Charge	T _J =25°C	---	9.5	---	nC

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. V_{DD}=25V, V_{GS}=10V, L=0.1mH, I_{AS}=50A., R_G=25Ω, Starting T_J=25°C.
3. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%.
4. Essentially independent of operating temperature.

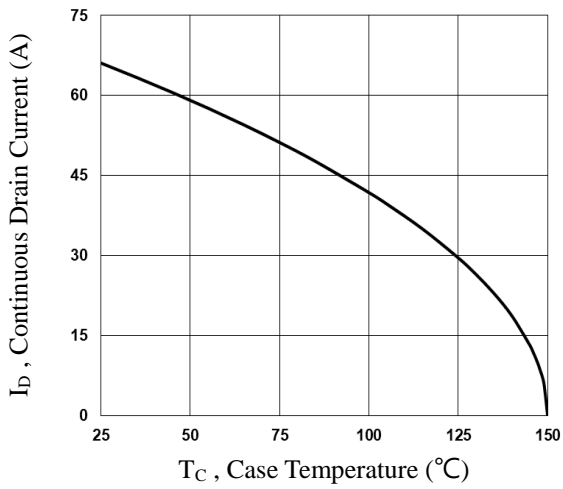


Fig.1 Continuous Drain Current vs. T_c

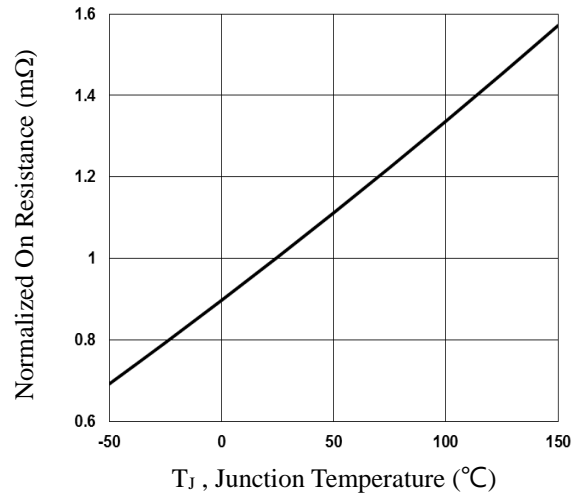


Fig.2 Normalized R_{DS(on)} vs. T_j

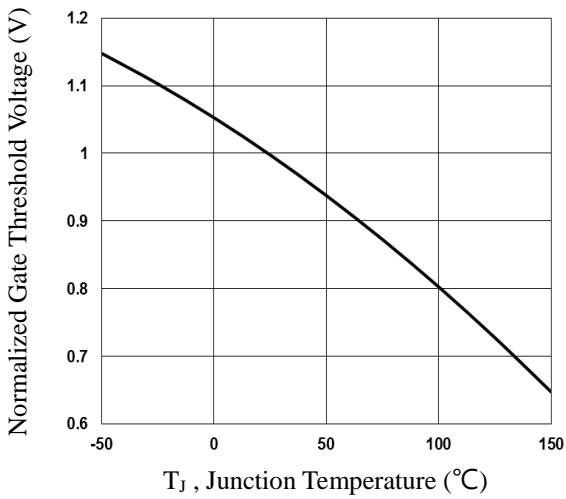


Fig.3 Normalized V_{th} vs. T_j

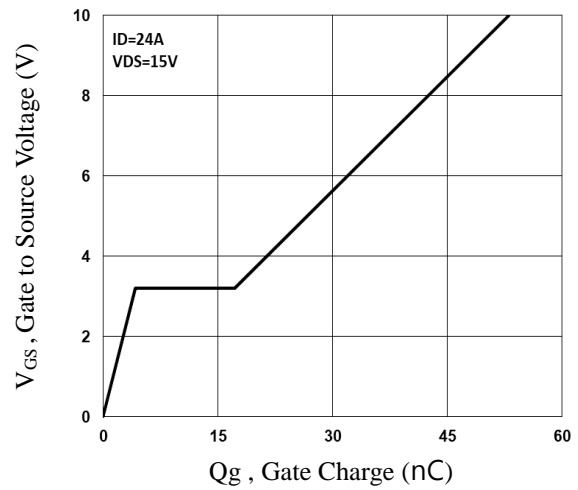


Fig.4 Gate Charge Waveform

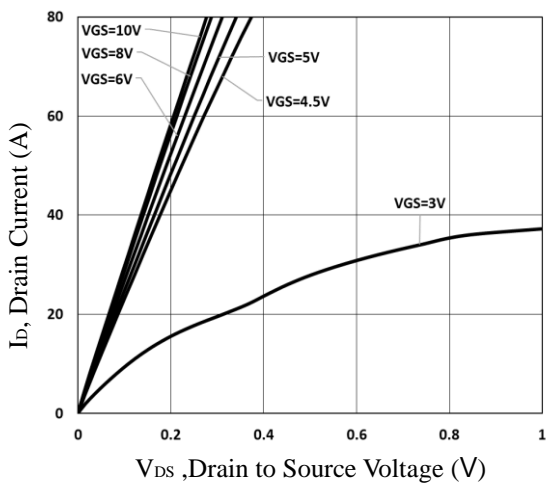


Fig.5 Typical Output Characteristics

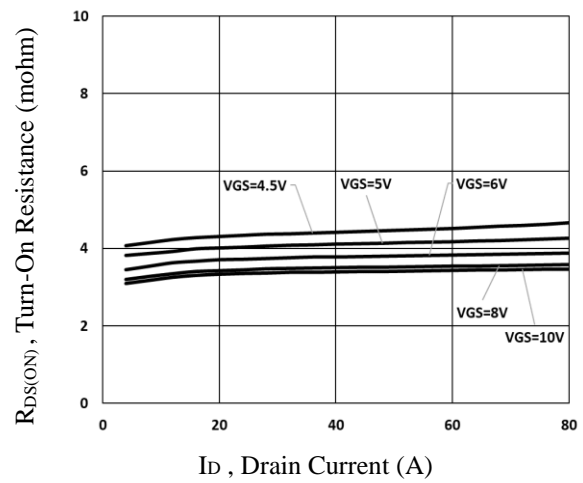


Fig.6 Turn-On Resistance vs. I_D

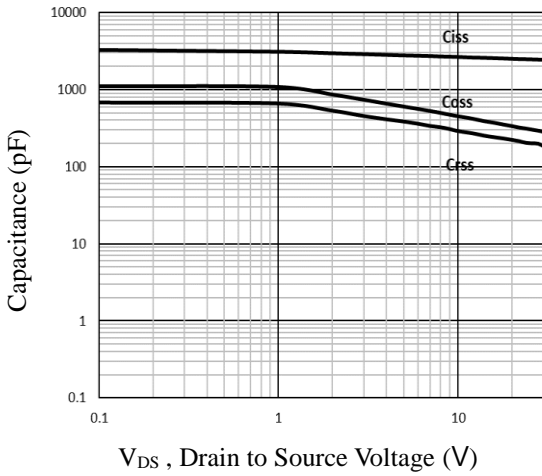


Fig.7 Capacitance Characteristics

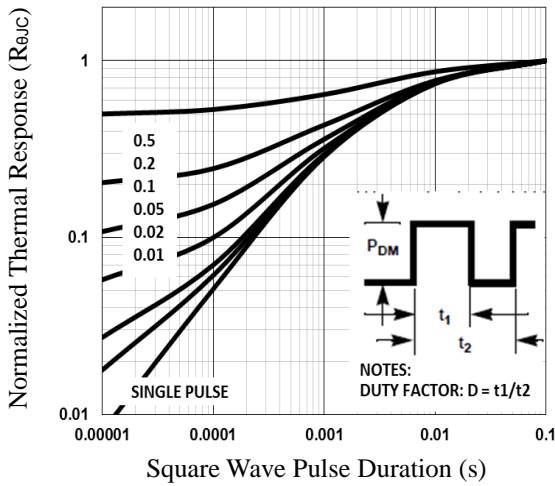


Fig.8 Normalized Transient Impedance

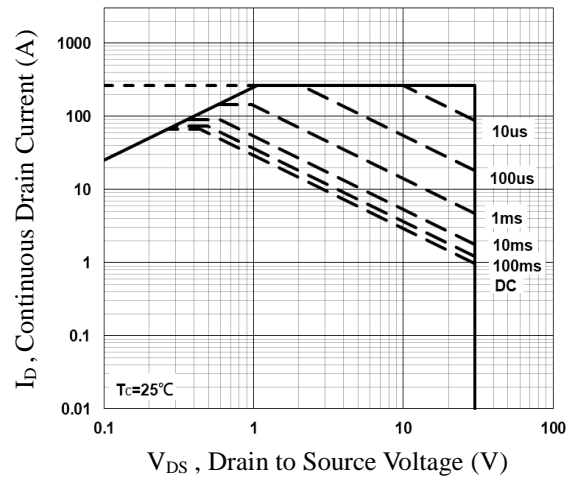


Fig.9 Maximum Safe Operation Area

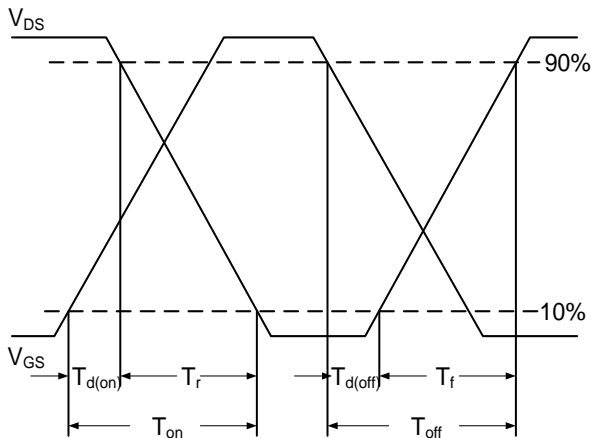


Fig.10 Switching Time Waveform

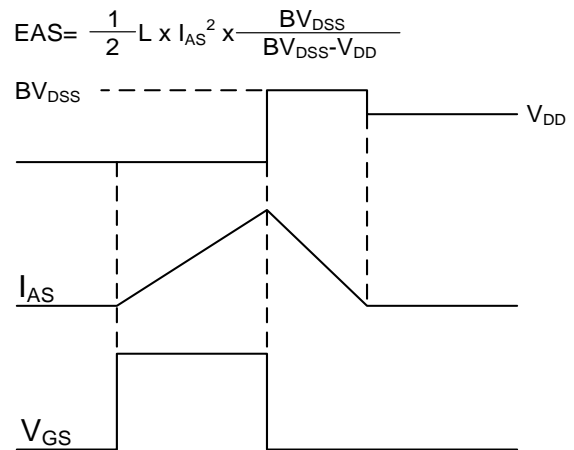
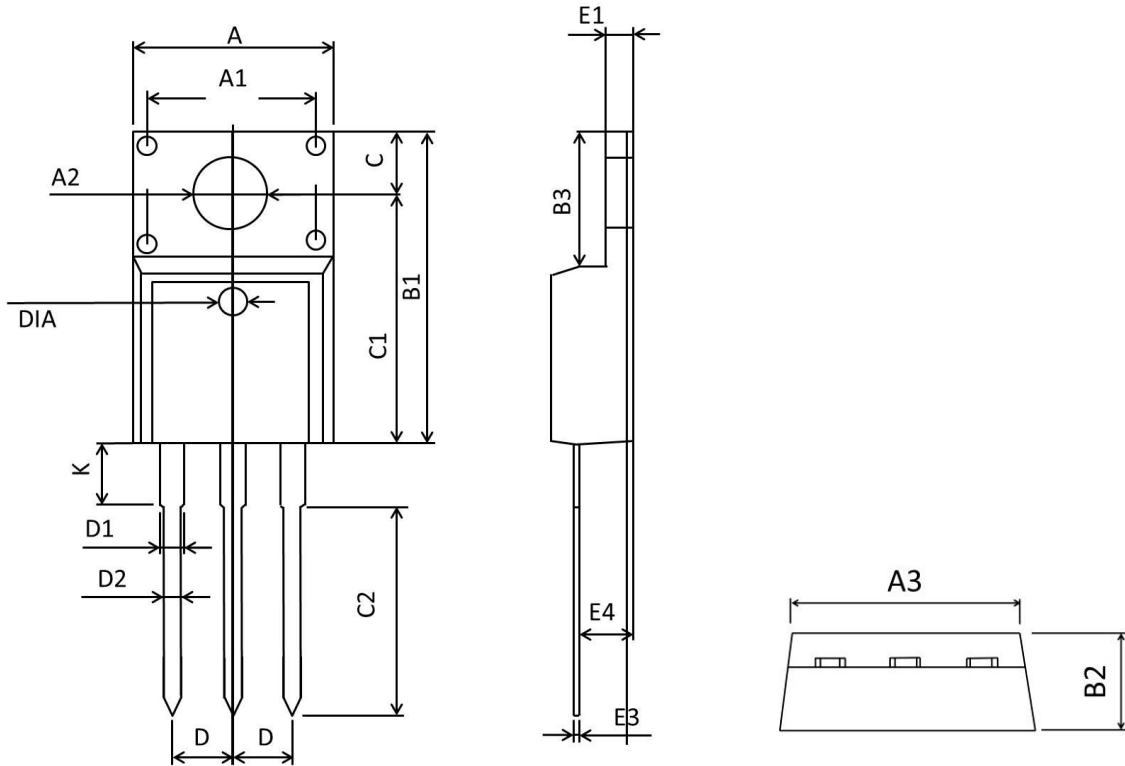


Fig.11 EAS Waveform

TO220F PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	9.860	10.460	0.389	0.411
A1	6.900	7.100	0.272	0.279
A2	3.100	3.500	0.123	0.137
B1	9.500	9.900	0.375	0.389
B2	4.500	4.900	0.178	0.192
B3	6.480	6.880	0.256	0.271
C	3.100	3.500	0.123	0.137
C1	12.270	12.870	0.484	0.506
C2	12.580	13.380	0.496	0.526
D	2.490	2.590	0.099	0.101
D1	1.070	1.470	0.043	0.057
D2	0.700	0.900	0.028	0.035
K	2.900	3.300	0.115	0.129
E1	2.340	2.740	0.093	0.107
E3	0.400	0.600	0.016	0.023
E4	2.560	2.960	0.101	0.116
DIA	1.45	1.55	0.058	0.061