

General Description

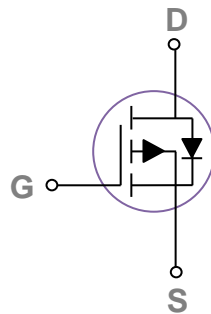
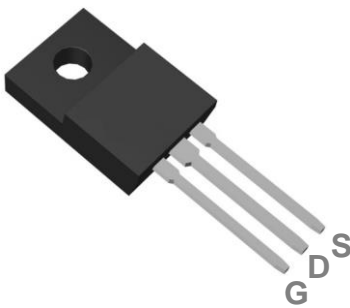
These P-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

BVDSS	R _{DS(ON)}	I _D
-100V	38mΩ	-25A

Features

- -100V,-25A, R_{DS(ON)} 38mΩ@V_{GS} = -10V
- Improved dv/dt capability
- Fast switching
- Green Device Available

TO220F Pin Configuration



Applications

- Networking
- Load Switch
- LED applications

Absolute Maximum Ratings (T_C=25°C unless otherwise noted)

Symbol	Parameter	Rating	Units
V _{DS}	Drain-Source Voltage	-100	V
V _{GS}	Gate-Source Voltage	±20	V
I _D	Drain Current – Continuous (T _C =25°C)	-25	A
	Drain Current – Continuous (T _C =100°C)	-16	A
I _{DM}	Drain Current – Pulsed ¹	-100	A
EAS	Single Pulse Avalanche Energy ²	180	mJ
IAS	Single Pulse Avalanche Current ²	-60	A
P _D	Power Dissipation (T _C =25°C)	68	W
	Power Dissipation – Derate above 25°C	0.54	W/°C
T _{STG}	Storage Temperature Range	-55 to 150	°C
T _J	Operating Junction Temperature Range	-55 to 150	°C

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
R _{θJA}	Thermal Resistance Junction to ambient	---	62	°C/W
R _{θJC}	Thermal Resistance Junction to Case	---	1.84	°C/W

Electrical Characteristics (T_J=25 °C, unless otherwise noted)
Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =-250μA	-100	---	---	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =-100V, V _{GS} =0V, T _J =25°C	---	---	-1	μA
		V _{DS} =-80V, V _{GS} =0V, T _J =85°C	---	---	-10	μA
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0V	---	---	±100	nA

On Characteristics

R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =-10V, I _D =-15A	---	32	38	mΩ
		V _{GS} =-4.5V, I _D =-10A	---	36	47	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =-250μA	-1.2	-1.6	-2.5	V

Dynamic and switching Characteristics

Q _g	Total Gate Charge ^{3,4}	V _{DS} =-50V, V _{GS} =-10V, I _D =-10A	---	92	140	nC
Q _{gs}	Gate-Source Charge ^{3,4}		---	13	20	
Q _{gd}	Gate-Drain Charge ^{3,4}		---	14	20	
T _{d(on)}	Turn-On Delay Time ^{3,4}	V _{DD} =-50V, V _{GS} =-10V, R _G =6Ω I _D =-10A	---	10	15	ns
T _r	Rise Time ^{3,4}		---	15	25	
T _{d(off)}	Turn-Off Delay Time ^{3,4}		---	20	30	
T _f	Fall Time ^{3,4}		---	25	40	
C _{iss}	Input Capacitance	V _{DS} =-50V, V _{GS} =0V, F=1MHz	---	5700	8550	pF
C _{oss}	Output Capacitance		---	160	240	
C _{rss}	Reverse Transfer Capacitance		---	120	180	

Guaranteed Avalanche Energy

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
EAS	Single Pulse Avalanche Energy	V _{DD} =50V, L=0.1mH, I _{AS} =46A	105.8	---	---	mJ

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current	V _G =V _D =0V, Force Current	---	---	-25	A
I _{SM}	Pulsed Source Current		---	---	-50	A
V _{SD}	Diode Forward Voltage	V _{GS} =0V, I _S =-1A, T _J =25°C	---	---	-1	V
t _{rr}	Reverse Recovery Time ³	V _R =-50V I _S =-10A	---	55	---	ns
Q _{rr}	Reverse Recovery Charge ³	di/dt=100A/μs T _J =25°C	---	60	---	nC

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. V_{DD}=-50V, V_{GS}=-10V, L=0.1mH, I_{AS}=-60A., Starting T_J=25°C
3. The data tested by pulsed, pulse width ≤ 300μs, duty cycle ≤ 2%.
4. Essentially independent of operating temperature.

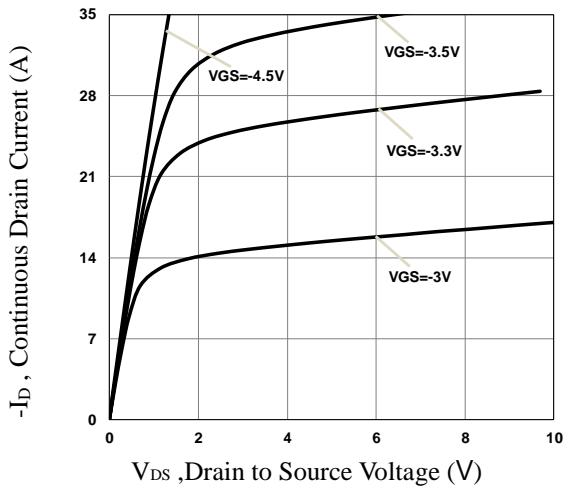


Fig.1 Typical Output Characteristics

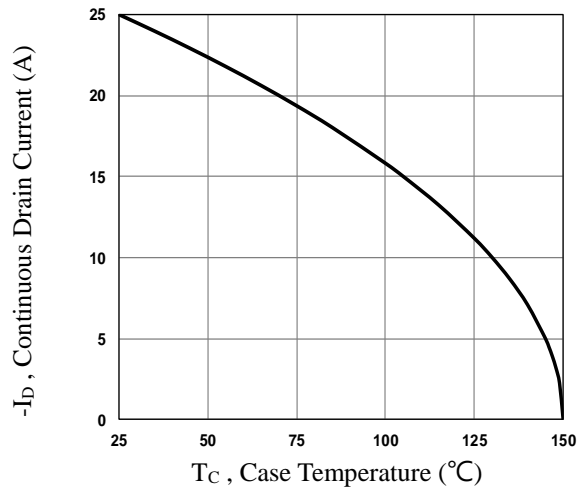


Fig.2 Continuous Drain Current vs. Tc

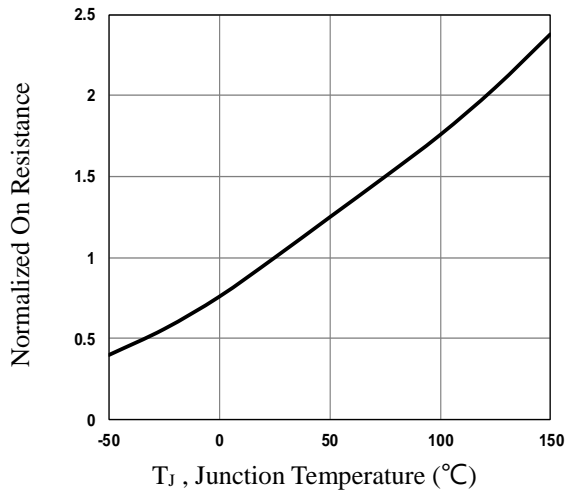


Fig.3 Normalized RDS(on) vs. Tj

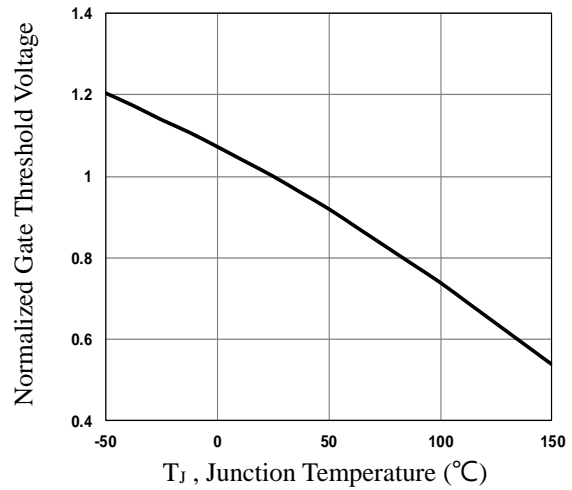


Fig.4 Normalized Vth vs. Tj

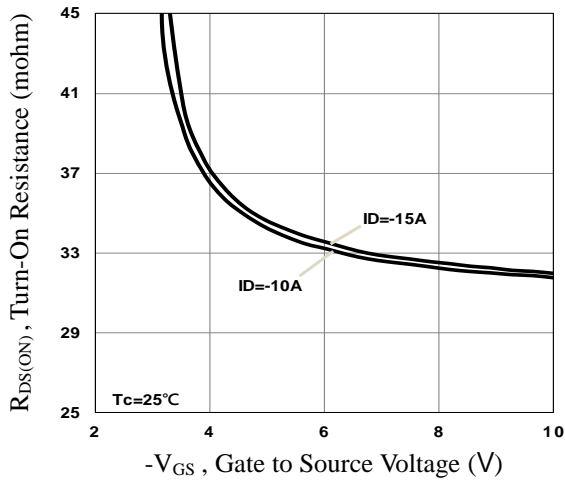


Fig.5 Turn-On Resistance vs. VGS

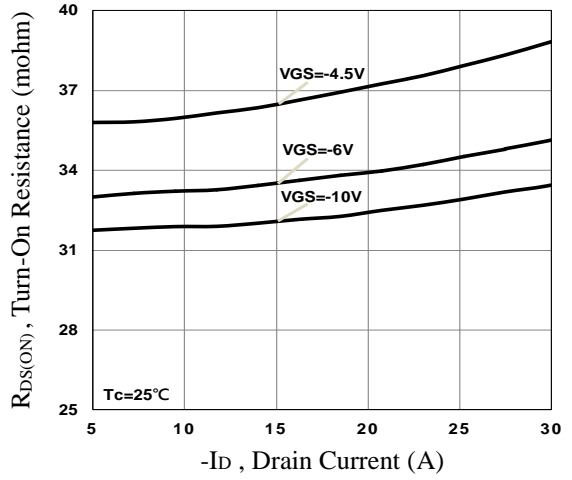


Fig.6 Turn-On Resistance vs. ID

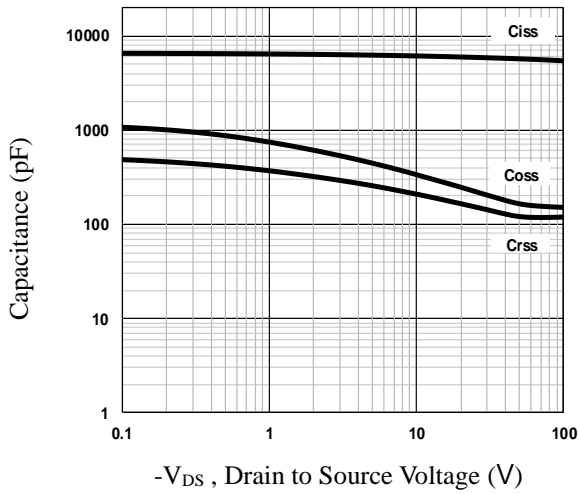


Fig.7 Capacitance Characteristics

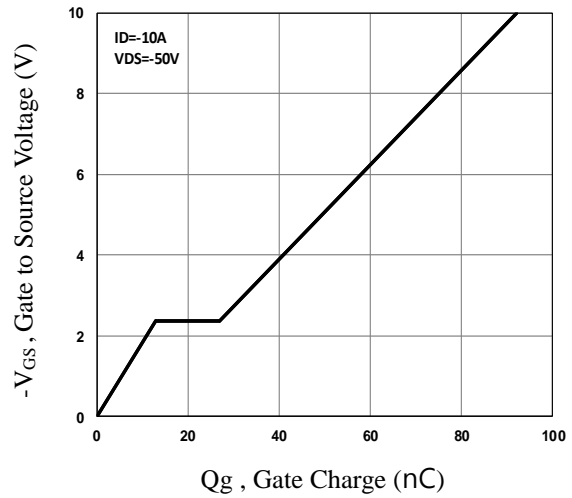


Fig.8 Gate Charge Characteristics

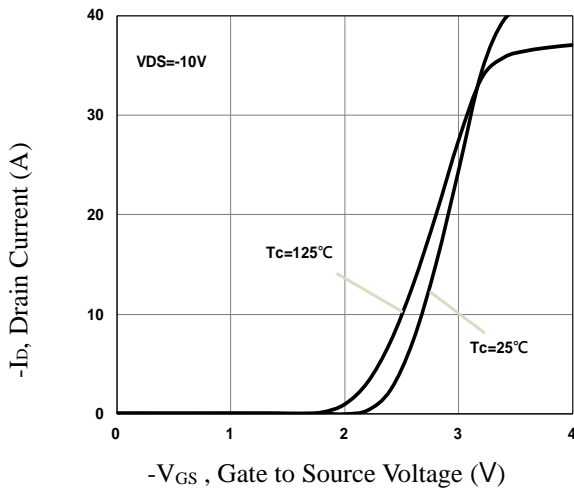


Fig.9 Transfer Characteristics

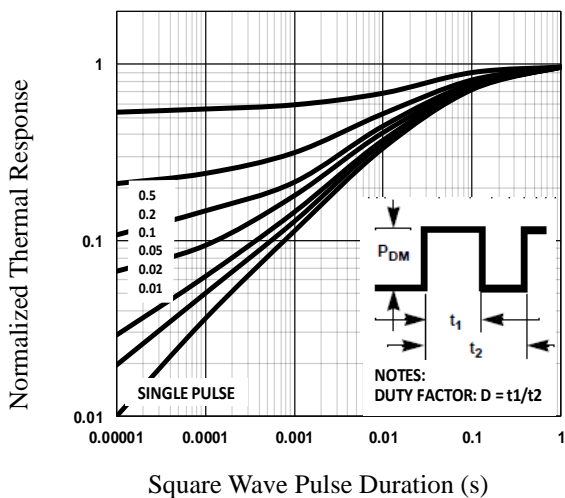


Fig.10 Normalized Transient Impedance

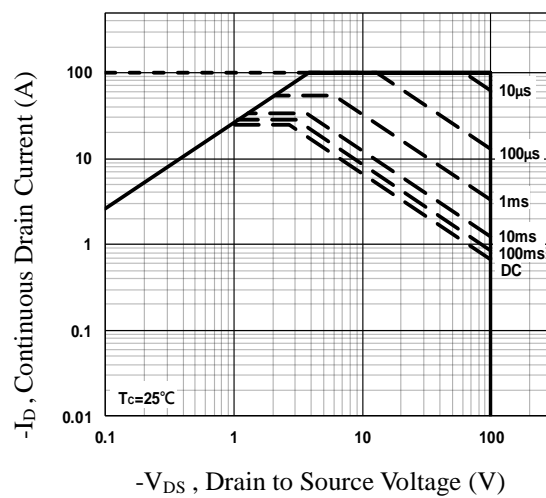
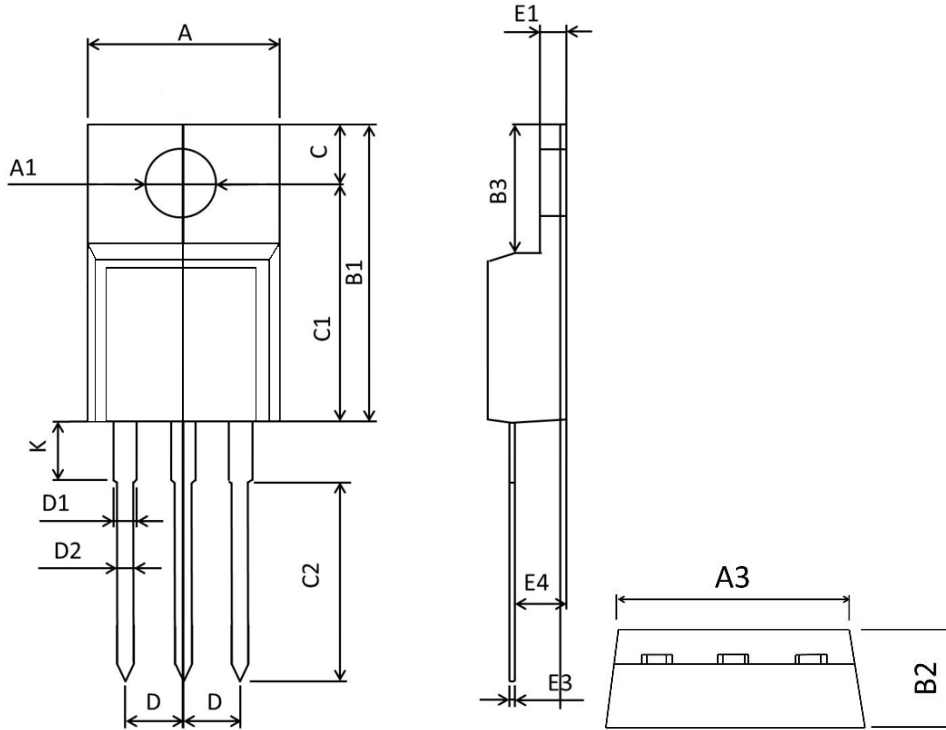


Fig.11 Maximum Safe Operation Area

TO220F PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	9.860	10.460	0.389	0.411
A1	3.100	3.500	0.122	0.138
B1	15.450	16.300	0.608	0.642
B2	4.400	5.000	0.173	0.197
B3	6.280	7.100	0.247	0.280
C	3.100	3.500	0.122	0.138
C1	12.270	12.870	0.483	0.507
C2	9.600	10.520	0.378	0.414
D	2.540BSC		0.1BSC	
D1	1.070	1.470	0.042	0.058
D2	0.600	1.000	0.024	0.039
K	2.800	3.500	0.110	0.138
E1	2.340	2.740	0.092	0.108
E3	0.350	0.650	0.014	0.026
E4	2.460	2.960	0.097	0.117