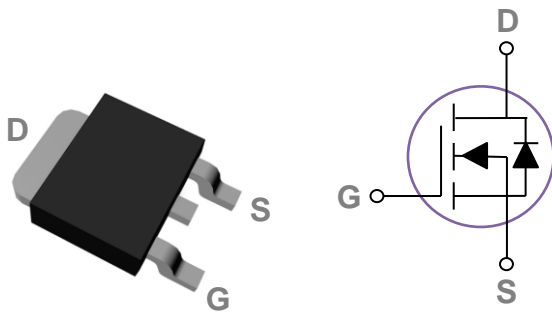


General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

TO252 Pin Configuration



BVDSS	RDSON	ID
80V	7.8mΩ	60A

Features

- 80V,60A, $R_{DS(ON)} = 7.8m\Omega @ V_{GS} = 10V$
- Improved dv/dt capability
- Fast switching
- Green Device Available

Applications

- Networking
- Load Switch
- LED applications
- Quick Charger

Absolute Maximum Ratings $T_c=25^\circ C$ unless otherwise noted

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	80	V
V_{GS}	Gate-Source Voltage	± 20	V
I_D	Drain Current – Continuous ($T_c=25^\circ C$)	60	A
	Drain Current – Continuous ($T_c=100^\circ C$)	38	A
I_{DM}	Drain Current – Pulsed ¹	240	A
EAS	Single Pulse Avalanche Energy ²	157	mJ
IAS	Single Pulse Avalanche Current ²	56	A
P_D	Power Dissipation ($T_c=25^\circ C$)	77	W
	Power Dissipation – Derate above $25^\circ C$	0.62	W/ $^\circ C$
T_{STG}	Storage Temperature Range	-55 to 150	$^\circ C$
T_J	Operating Junction Temperature Range	-55 to 150	$^\circ C$

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	1.62	$^\circ C/W$

Electrical Characteristics (T_J=25 °C, unless otherwise noted)
Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250μA	80	---	---	V
I _{DSS}	Drain-Source Leakage Current	V _{DS} =80V, V _{GS} =0V, T _J =25°C	---	---	1	μA
		V _{DS} =64V, V _{GS} =0V, T _J =85°C	---	---	10	μA
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±20V, V _{DS} =0V	---	---	±100	nA

On Characteristics

R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =10V, I _D =15A	---	6.5	7.8	mΩ
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250μA	2	3	4	V

Dynamic and switching Characteristics

Q _g	Total Gate Charge ^{3, 4}	V _{DS} =40V, V _{GS} =10V, I _D =30A	---	22.5	35	nC
Q _{gs}	Gate-Source Charge ^{3, 4}		---	4.7	7	
Q _{gd}	Gate-Drain Charge ^{3, 4}		---	5.5	8.5	
T _{d(on)}	Turn-On Delay Time ^{3, 4}	V _{DD} =40V, V _{GS} =10V, R _G =6Ω I _D =30A	---	20	30	ns
T _r	Rise Time ^{3, 4}		---	15	25	
T _{d(off)}	Turn-Off Delay Time ^{3, 4}		---	40	60	
T _f	Fall Time ^{3, 4}		---	30	45	
C _{iss}	Input Capacitance	V _{DS} =40V, V _{GS} =0V, F=1MHz	---	1450	2200	pF
C _{oss}	Output Capacitance		---	370	560	
C _{rss}	Reverse Transfer Capacitance		---	10	15	
R _g	Gate resistance	V _{GS} =0V, V _{DS} =0V, F=1MHz	---	1	---	Ω

Guaranteed Avalanche Energy

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
EAS	Single Pulse Avalanche Energy	V _{DD} =25V, L=0.1mH, I _{AS} =36A	65	---	---	mJ

Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current	V _G =V _D =0V, Force Current	---	---	60	A
I _{SM}	Pulsed Source Current		---	---	120	A
V _{SD}	Diode Forward Voltage	V _{GS} =0V, I _S =1A, T _J =25°C	---	---	1	V
t _{rr}	Reverse Recovery Time	V _R =50V, I _S =10A	---	40	---	ns
Q _{rr}	Reverse Recovery Charge	di/dt=100A/μs, T _J =25°C	---	55	---	nC

Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. V_{DD}=25V, V_{GS}=10V, L=0.1mH, I_{AS}=56A., R_G=25Ω, Starting T_J=25°C.
3. The data tested by pulsed , pulse width ≤ 300μs , duty cycle ≤ 2%.
4. Essentially independent of operating temperature.

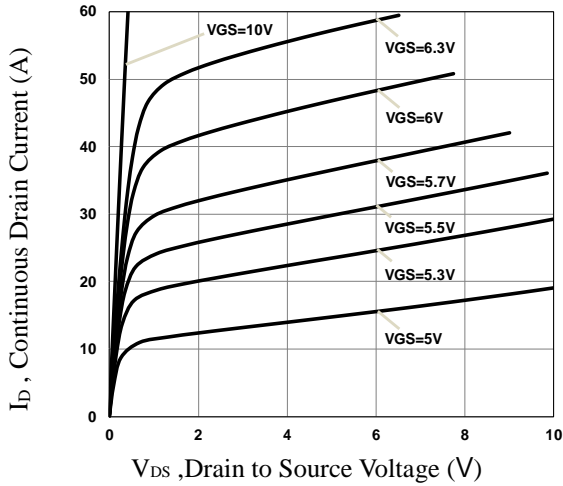


Fig.1 Typical Output Characteristics

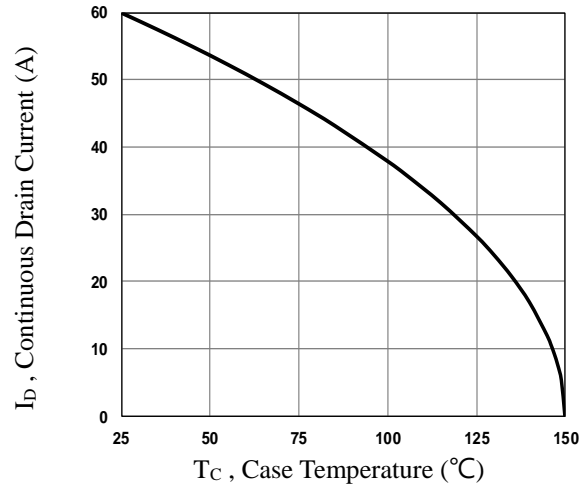


Fig.2 Continuous Drain Current vs. T_c

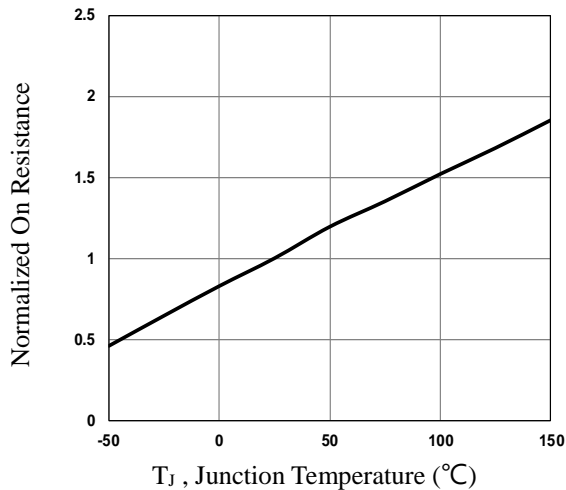


Fig.3 Normalized $R_{DS(on)}$ vs. T_J

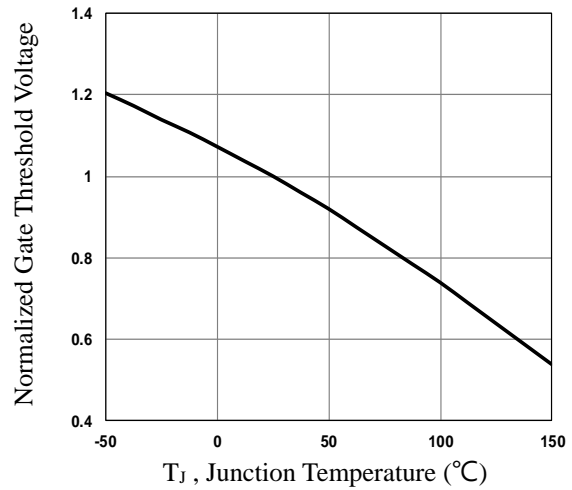


Fig.4 Normalized V_{th} vs. T_J

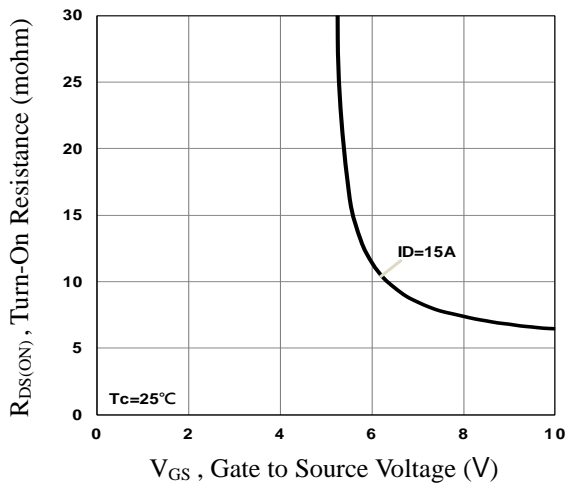


Fig.5 Turn-On Resistance vs. V_{GS}

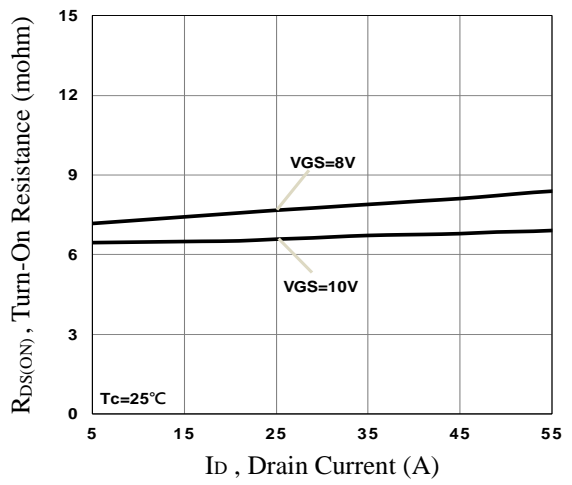


Fig.6 Turn-On Resistance vs. I_D

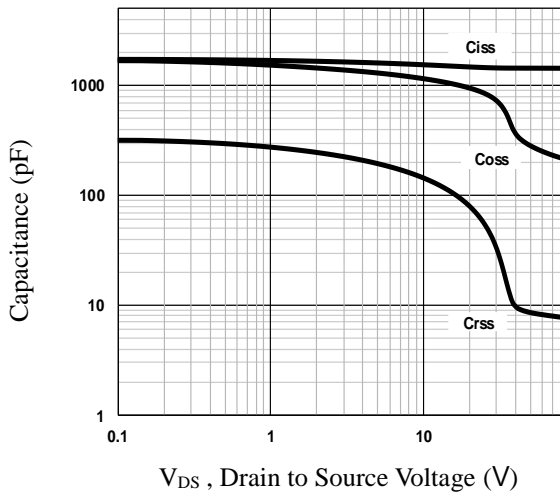


Fig.7 Capacitance Characteristics

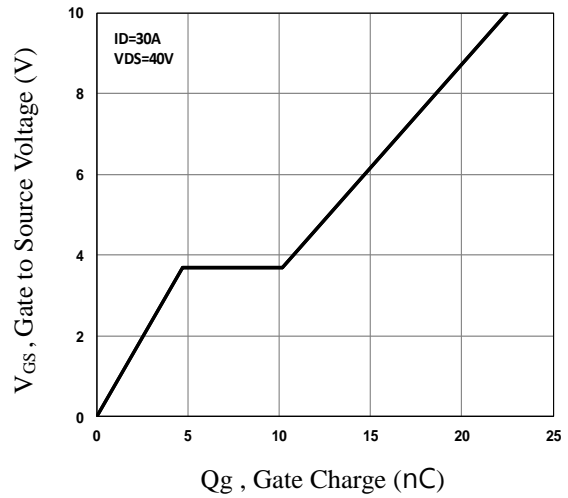


Fig.8 Gate Charge Characteristics

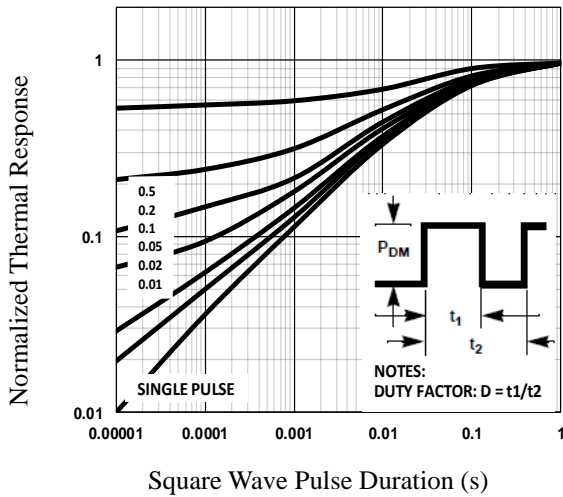


Fig.9 Normalized Transient Impedance

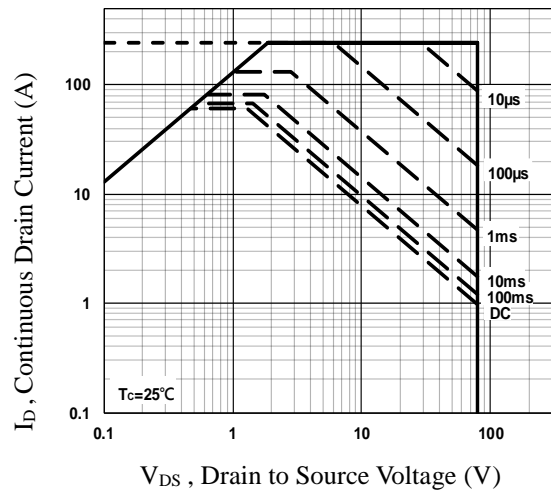


Fig.10 Maximum Safe Operation Area

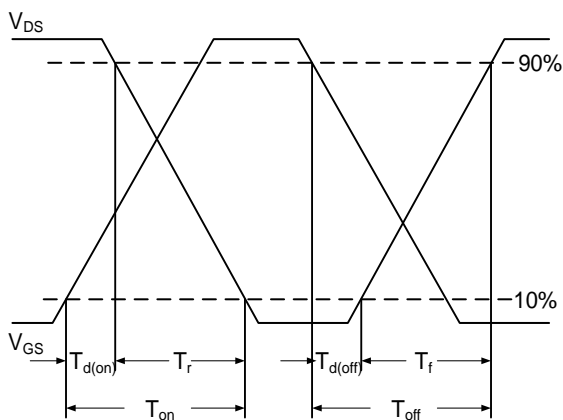


Fig.11 Switching Time Waveform

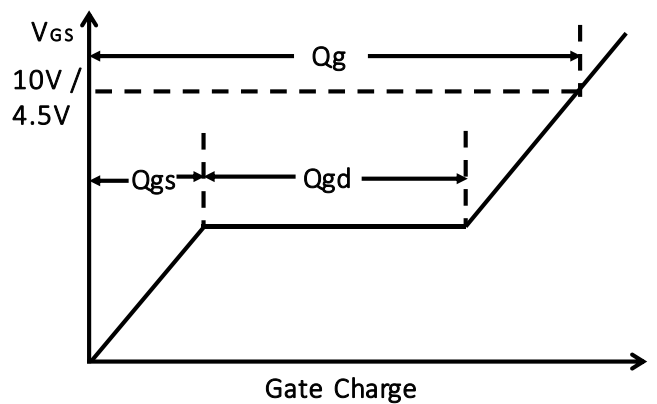
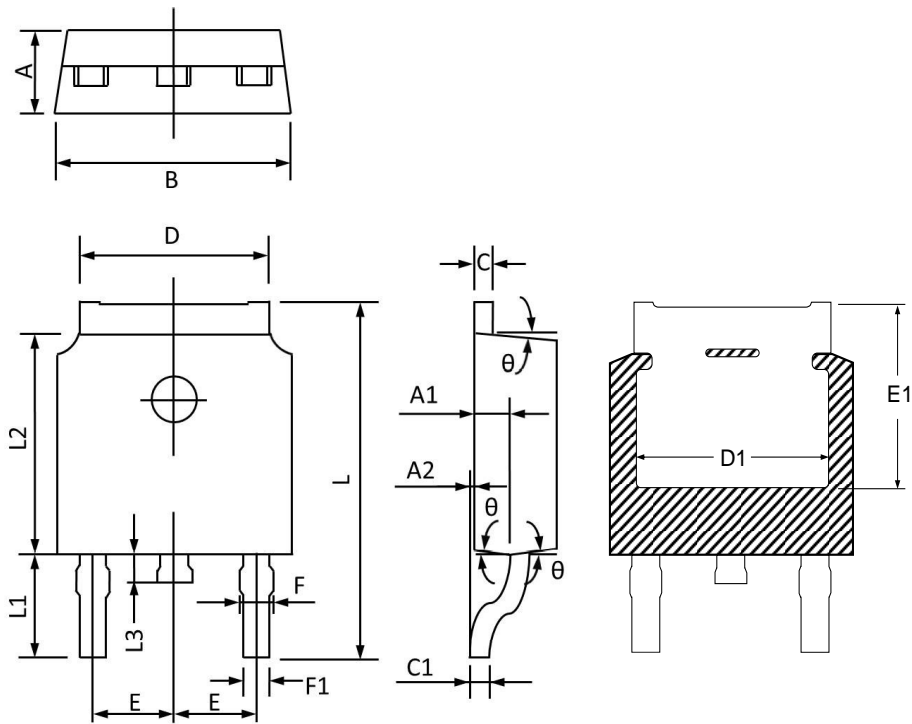


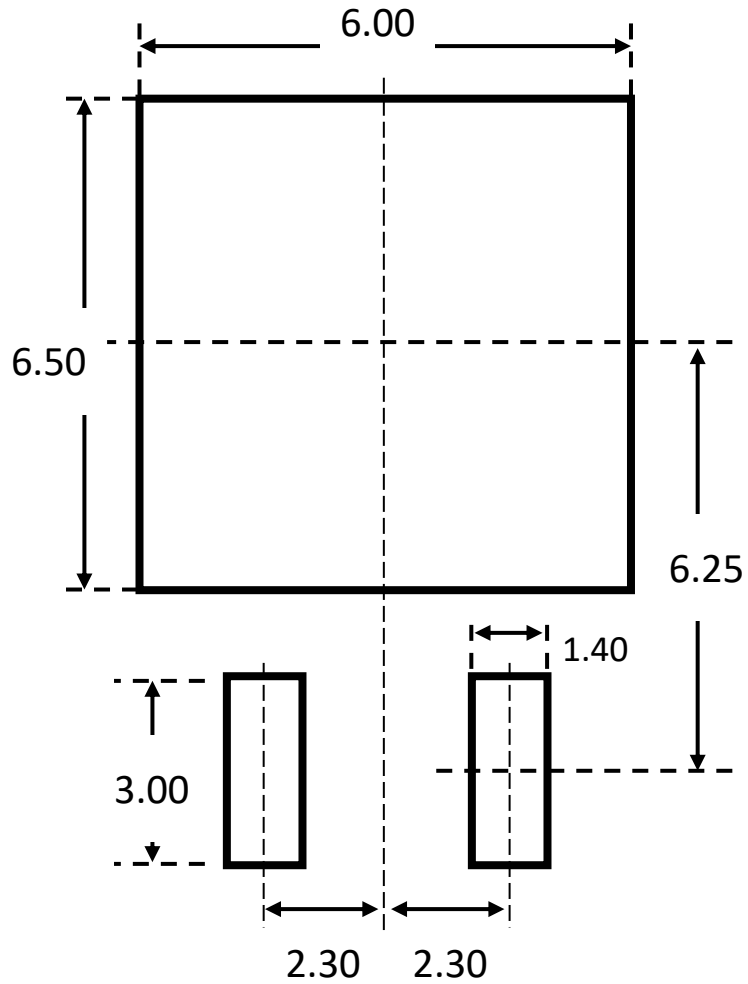
Fig.12 Gate Charge Waveform

TO252 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	2.450	2.150	0.096	0.085
A1	1.200	0.900	0.047	0.035
A2	0.250	0.000	0.010	0.000
B	6.800	6.300	0.268	0.248
C	0.600	0.350	0.024	0.014
C1	0.600	0.380	0.024	0.015
D	5.500	5.100	0.217	0.201
D1	5.400	4.950	0.212	0.195
E	2.400	2.000	0.094	0.079
E1	5.650	4.950	0.222	0.194
F	1.150	0.600	0.045	0.024
F1	0.900	0.500	0.035	0.020
L	10.400	9.400	0.409	0.370
L1	3.100	2.400	0.122	0.094
L2	6.300	5.300	0.248	0.209
L3	1.200	0.600	0.047	0.024
θ	9°	3°	9°	3°

TO252 RECOMMENDED LAND PATTERN



unit : mm