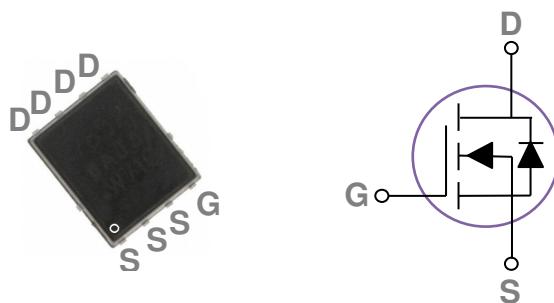


### General Description

These N-Channel enhancement mode power field effect transistors are using trench DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency fast switching applications.

### PPAK5X6 Pin Configuration



BVDSS	RDS(ON)	ID
65V	13.5mΩ	48A

### Features

- 65V,48A,  $RDS(ON) = 13.5m\Omega @ VGS = 10V$
- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

### Applications

- Motor Drive
- Power Tools
- LED Lighting

### Absolute Maximum Ratings $T_c=25^\circ C$ unless otherwise noted

Symbol	Parameter	Rating	Units
$V_{DS}$	Drain-Source Voltage	65	V
$V_{GS}$	Gate-Source Voltage	$\pm 20$	V
$I_D$	Drain Current – Continuous ( $T_c=25^\circ C$ )	48	A
	Drain Current – Continuous ( $T_c=100^\circ C$ )	30.4	A
$I_{DM}$	Drain Current – Pulsed <sup>1</sup>	192	A
EAS	Single Pulse Avalanche Energy <sup>2</sup>	61	mJ
IAS	Single Pulse Avalanche Current <sup>2</sup>	35	A
$P_D$	Power Dissipation ( $T_c=25^\circ C$ )	96	W
	Power Dissipation – Derate above $25^\circ C$	0.77	W/ $^\circ C$
$T_{STG}$	Storage Temperature Range	-55 to 150	$^\circ C$
$T_J$	Operating Junction Temperature Range	-55 to 150	$^\circ C$

### Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62	$^\circ C/W$
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	1.3	$^\circ C/W$

**Electrical Characteristics ( $T_J=25\text{ }^{\circ}\text{C}$ , unless otherwise noted)**
**Off Characteristics**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$BV_{DSS}$	Drain-Source Breakdown Voltage	$V_{GS}=0\text{V}$ , $I_D=250\mu\text{A}$	65	---	---	V
$\Delta BV_{DSS}/\Delta T_J$	$BV_{DSS}$ Temperature Coefficient	Reference to $25\text{ }^{\circ}\text{C}$ , $I_D=1\text{mA}$	---	0.03	---	$\text{V}/\text{C}$
$I_{DSS}$	Drain-Source Leakage Current	$V_{DS}=60\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=25\text{ }^{\circ}\text{C}$	---	---	1	$\mu\text{A}$
		$V_{DS}=48\text{V}$ , $V_{GS}=0\text{V}$ , $T_J=125\text{ }^{\circ}\text{C}$	---	---	10	$\mu\text{A}$
$I_{GSS}$	Gate-Source Leakage Current	$V_{GS}=\pm 20\text{V}$ , $V_{DS}=0\text{V}$	---	---	$\pm 100$	nA

**On Characteristics**

$R_{DS(\text{ON})}$	Static Drain-Source On-Resistance <sup>3</sup>	$V_{GS}=10\text{V}$ , $I_D=20\text{A}$	---	11.5	13.5	$\text{m}\Omega$
		$V_{GS}=4.5\text{V}$ , $I_D=10\text{A}$	---	14.5	18	$\text{m}\Omega$
$V_{GS(\text{th})}$	Gate Threshold Voltage	$V_{GS}=V_{DS}$ , $I_D = 250\mu\text{A}$	1.2	1.6	2.2	V
$\Delta V_{GS(\text{th})}$	$V_{GS(\text{th})}$ Temperature Coefficient		---	-4	---	$\text{mV}/\text{C}$
$g_{fs}$	Forward Transconductance	$V_{DS}=10\text{V}$ , $I_D=6\text{A}$	---	11.7	---	S

**Dynamic and switching Characteristics**

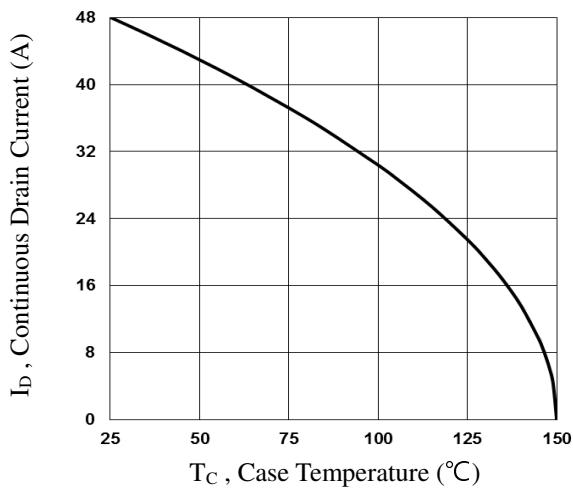
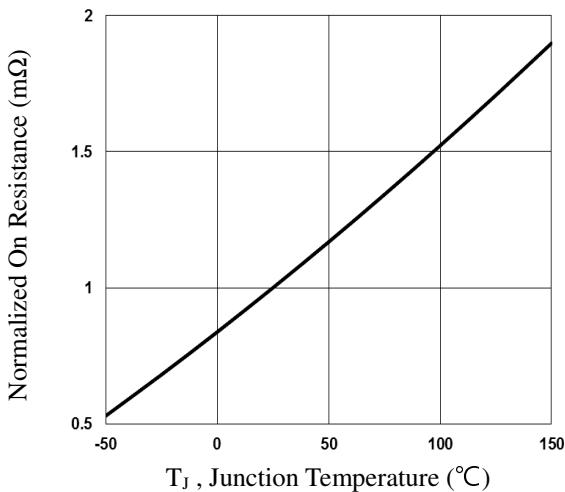
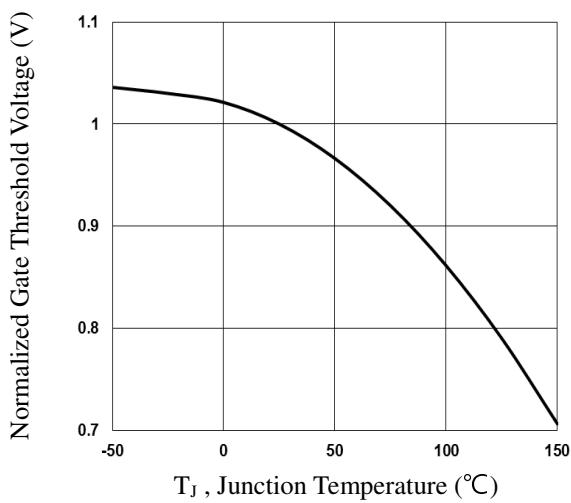
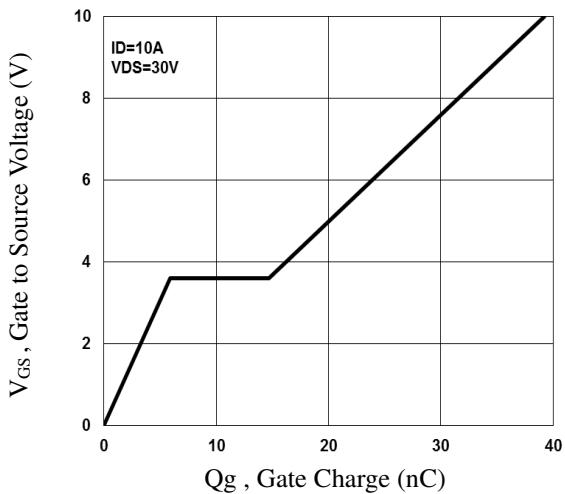
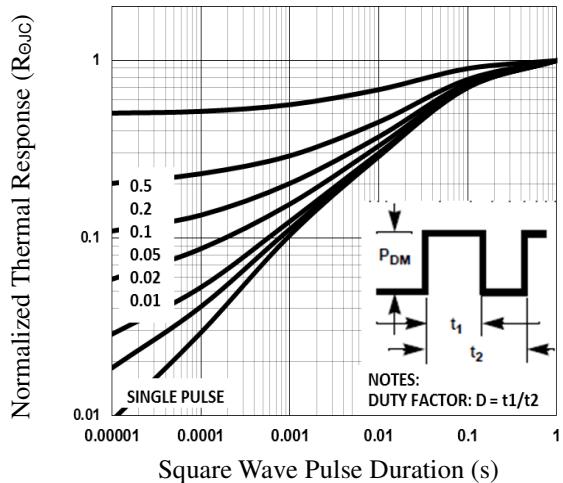
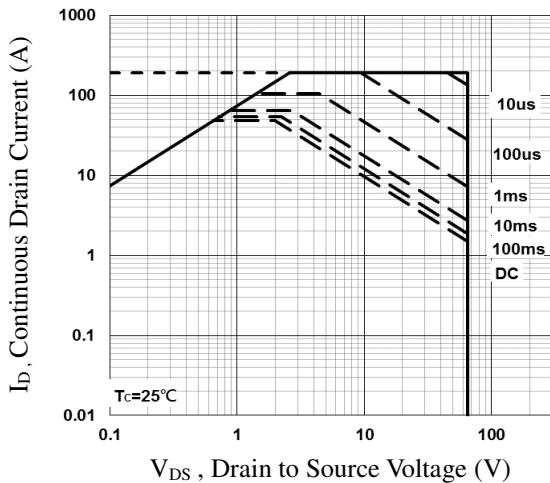
$Q_g$	Total Gate Charge <sup>3, 4</sup>	$V_{DS}=30\text{V}$ , $V_{GS}=10\text{V}$ , $I_D=10\text{A}$	---	39.2	59	nC
$Q_{gs}$	Gate-Source Charge <sup>3, 4</sup>		---	5.9	9	
$Q_{gd}$	Gate-Drain Charge <sup>3, 4</sup>		---	8.8	14	
$T_{d(on)}$	Turn-On Delay Time <sup>3, 4</sup>	$V_{DD}=15\text{V}$ , $V_{GS}=10\text{V}$ , $R_G=6\Omega$ $I_D=1\text{A}$	---	9.6	18	ns
$T_r$	Rise Time <sup>3, 4</sup>		---	28.2	54	
$T_{d(off)}$	Turn-Off Delay Time <sup>3, 4</sup>		---	45.3	86	
$T_f$	Fall Time <sup>3, 4</sup>		---	10.9	21	
$C_{iss}$	Input Capacitance	$V_{DS}=25\text{V}$ , $V_{GS}=0\text{V}$ , $F=1\text{MHz}$	---	2100	3050	pF
$C_{oss}$	Output Capacitance		---	165	240	
$C_{rss}$	Reverse Transfer Capacitance		---	80	120	
$R_g$	Gate resistance	$V_{GS}=0\text{V}$ , $V_{DS}=0\text{V}$ , $F=1\text{MHz}$	---	1.6	3.2	$\Omega$

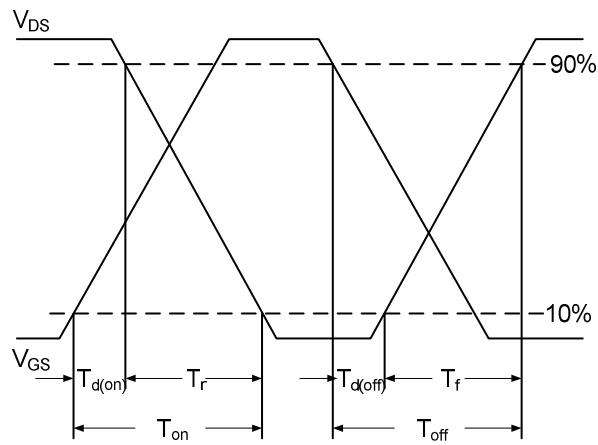
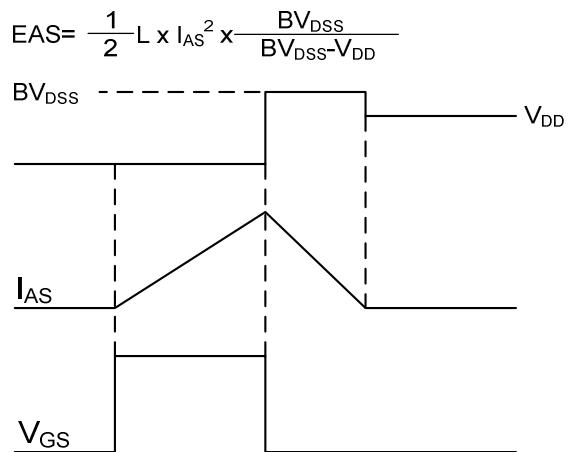
**Drain-Source Diode Characteristics and Maximum Ratings**

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
$I_s$	Continuous Source Current	$V_G=V_D=0\text{V}$ , Force Current	---	---	48	A
$I_{SM}$	Pulsed Source Current <sup>3</sup>		---	---	96	A
$V_{SD}$	Diode Forward Voltage <sup>3</sup>	$V_{GS}=0\text{V}$ , $I_s=1\text{A}$ , $T_J=25\text{ }^{\circ}\text{C}$	---	---	1	V

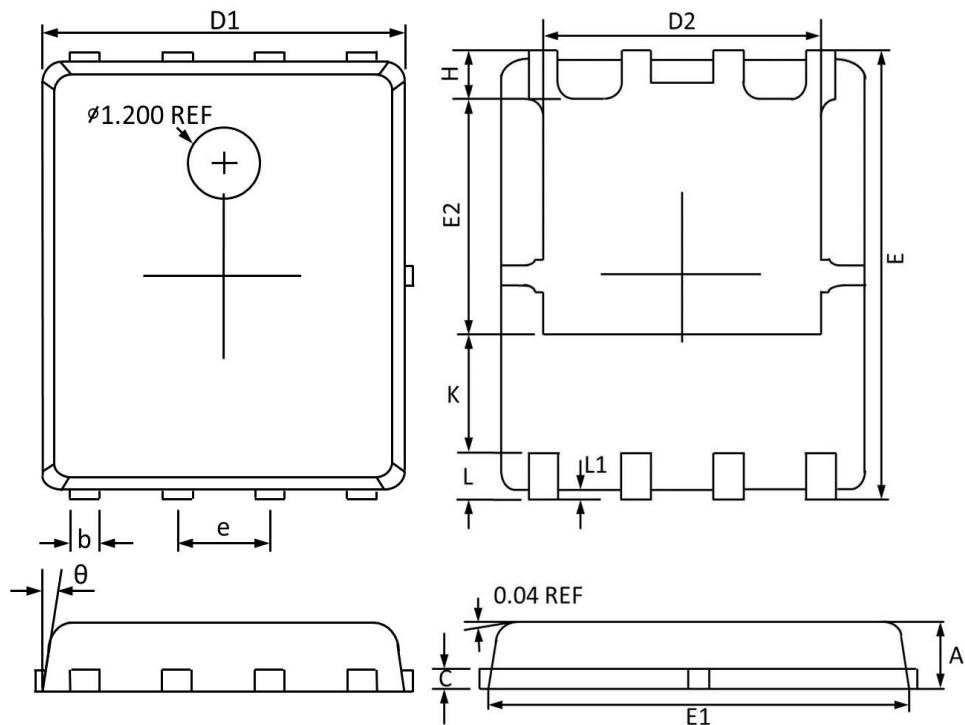
Note :

1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2.  $V_{DD}=25\text{V}$ ,  $V_{GS}=10\text{V}$ ,  $L=0.1\text{mH}$ ,  $I_{AS}=35\text{A}$ ,  $R_G=25\Omega$ , Starting  $T_J=25\text{ }^{\circ}\text{C}$ .
3. The data tested by pulsed , pulse width  $\leq 300\text{us}$  , duty cycle  $\leq 2\%$ .
4. Essentially independent of operating temperature.


**Fig.1 Continuous Drain Current vs. TC**

**Fig.2 Normalized RDSON vs. TJ**

**Fig.3 Normalized Vth vs. TJ**

**Fig.4 Gate Charge Waveform**

**Fig.5 Normalized Transient Response**

**Fig.6 Maximum Safe Operation Area**


**Fig.7 Switching Time Waveform**

**Fig.8 EAS Waveform**

## PPAK5x6 PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MAX	MIN	MAX	MIN
A	<b>1.100</b>	<b>0.800</b>	<b>0.043</b>	<b>0.031</b>
b	<b>0.510</b>	<b>0.330</b>	<b>0.020</b>	<b>0.013</b>
C	<b>0.300</b>	<b>0.200</b>	<b>0.012</b>	<b>0.008</b>
D1	<b>5.100</b>	<b>4.800</b>	<b>0.201</b>	<b>0.189</b>
D2	<b>4.100</b>	<b>3.610</b>	<b>0.161</b>	<b>0.142</b>
E	<b>6.200</b>	<b>5.900</b>	<b>0.244</b>	<b>0.232</b>
E1	<b>5.900</b>	<b>5.700</b>	<b>0.232</b>	<b>0.224</b>
E2	<b>3.780</b>	<b>3.350</b>	<b>0.149</b>	<b>0.132</b>
e	<b>1.27 BSC</b>		<b>0.05 BSC</b>	
H	<b>0.700</b>	<b>0.410</b>	<b>0.028</b>	<b>0.016</b>
K	<b>1.500</b>	<b>1.100</b>	<b>0.059</b>	<b>0.043</b>
L	<b>0.710</b>	<b>0.510</b>	<b>0.028</b>	<b>0.020</b>
L1	<b>0.200</b>	<b>0.060</b>	<b>0.008</b>	<b>0.002</b>
θ	<b>12°</b>	<b>0°</b>	<b>12°</b>	<b>0°</b>