

General Description

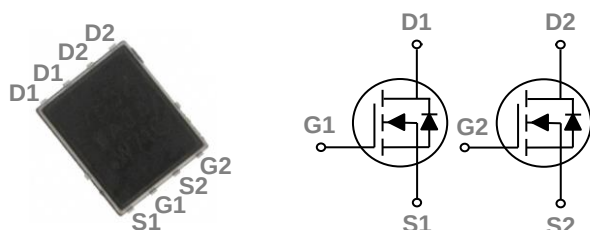
These N-Channel enhancement mode power field effect transistors are planar stripe, DMOS technology. This advanced technology has been especially tailored to minimize on-state resistance, provide superior switching performance, and withstand high energy pulse in the avalanche and commutation mode. These devices are well suited for high efficiency switch mode power supply

BVDSS	RDSON	ID
600V	5.0Ω	2A

Features

- Improved dv/dt capability
- Fast switching
- 100% EAS Guaranteed
- Green Device Available

PPAK5x6 Dual Pin Configuration



Applications

- High efficient switched mode power supplies
- TV Power
- Adapter/charger
- LED Lighting

Absolute Maximum Ratings $T_c=25^{\circ}\text{C}$ unless otherwise noted

Symbol	Parameter	Rating	Units
V_{DS}	Drain-Source Voltage	600	V
V_{GS}	Gate-Source Voltage	± 30	V
I_D	Drain Current – Continuous ($T_c=25^{\circ}\text{C}$)	2	A
	Drain Current – Continuous ($T_c=100^{\circ}\text{C}$)	1.3	A
I_{DM}	Drain Current – Pulsed ¹	8	A
EAS	Single Pulse Avalanche Energy ²	65	mJ
IAS	Single Pulse Avalanche Current ²	3.6	A
P_D	Power Dissipation ($T_c=25^{\circ}\text{C}$)	63	W
	Power Dissipation – Derate above 25°C	0.5	W/ $^{\circ}\text{C}$
T_{STG}	Storage Temperature Range	-55 to 150	$^{\circ}\text{C}$
T_J	Operating Junction Temperature Range	-55 to 150	$^{\circ}\text{C}$

Thermal Characteristics

Symbol	Parameter	Typ.	Max.	Unit
$R_{\theta JA}$	Thermal Resistance Junction to ambient	---	62	$^{\circ}\text{C}/\text{W}$
$R_{\theta JC}$	Thermal Resistance Junction to Case	---	2	$^{\circ}\text{C}/\text{W}$

Electrical Characteristics (T_J=25 °C, unless otherwise noted)
Off Characteristics

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
BV _{DSS}	Drain-Source Breakdown Voltage	V _{GS} =0V, I _D =250uA	600	---	---	V
ΔBV _{DSS} /ΔT _J	BV _{DSS} Temperature Coefficient	Reference to 25°C, I _D =1mA	---	0.6	---	V/°C
I _{DSS}	Drain-Source Leakage Current	V _{DS} =600V, V _{GS} =0V, T _J =25°C	---	---	1	uA
		V _{DS} =480V, V _{GS} =0V, T _J =125°C	---	---	10	uA
I _{GSS}	Gate-Source Leakage Current	V _{GS} =±30V, V _{DS} =0V	---	---	±100	nA

On Characteristics

R _{DS(ON)}	Static Drain-Source On-Resistance	V _{GS} =10V, I _D =1A	---	4.3	5	Ω
V _{GS(th)}	Gate Threshold Voltage	V _{GS} =V _{DS} , I _D =250uA	3	4	5	V
ΔV _{GS(th)}	V _{GS(th)} Temperature Coefficient		---	-7	---	mV/°C
g _{fs}	Forward Transconductance	V _{DS} =10V, I _D =1A	---	2.7	---	S

Dynamic and switching Characteristics

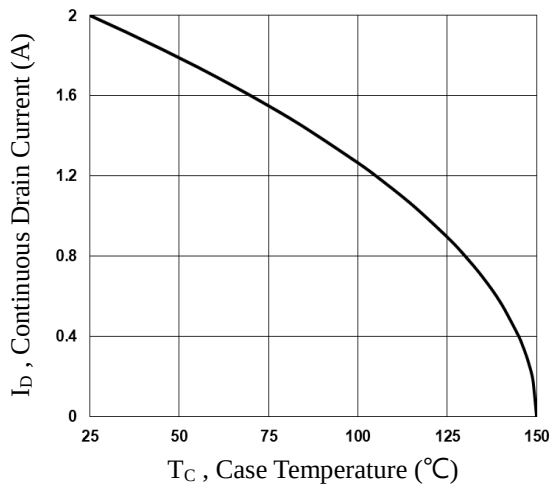
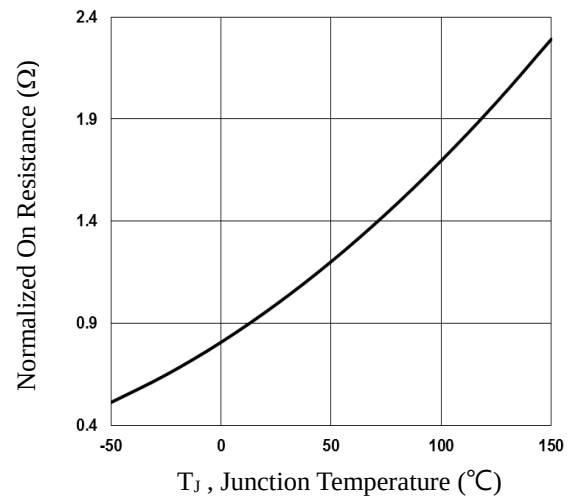
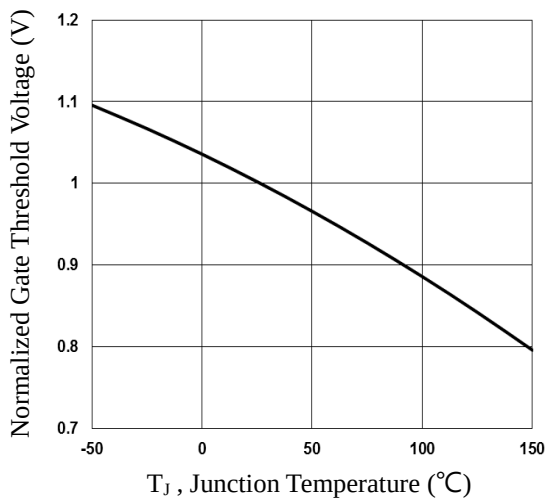
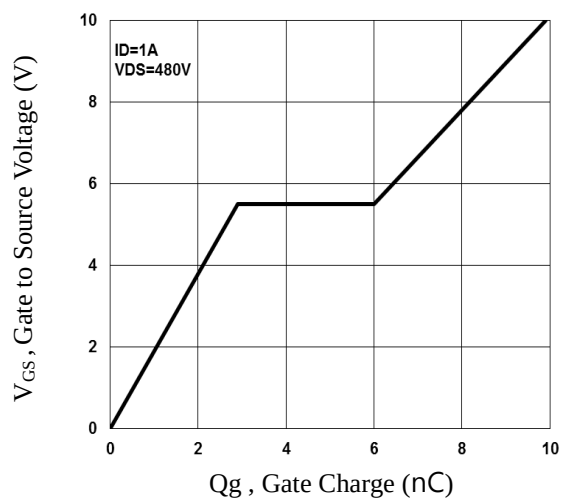
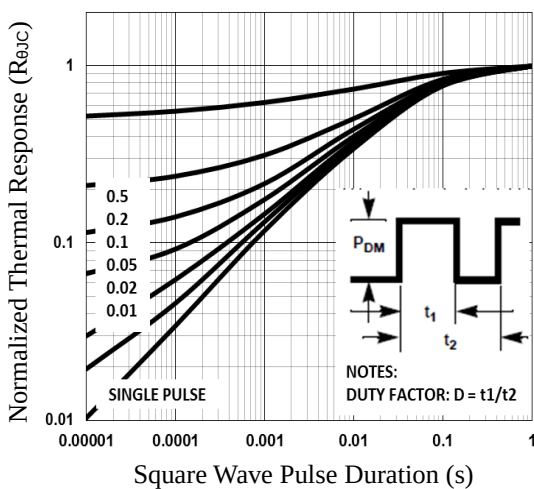
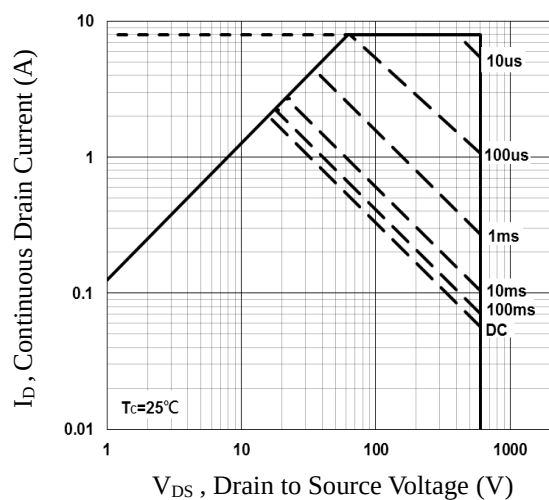
Q _g	Total Gate Charge ^{3,4}	V _{DS} =480V, V _{GS} =10V, I _D =1A	---	9.9	15	nC
Q _{gs}	Gate-Source Charge ^{3,4}		---	2.9	6	
Q _{gd}	Gate-Drain Charge ^{3,4}		---	3.1	6	
T _{d(on)}	Turn-On Delay Time ^{3,4}	V _{DD} =300V, V _{GS} =10V, R _G =25Ω I _D =1A	---	16.6	32	ns
T _r	Rise Time ^{3,4}		---	6.8	13	
T _{d(off)}	Turn-Off Delay Time ^{3,4}		---	23.1	44	
T _f	Fall Time ^{3,4}		---	17	33	
C _{iss}	Input Capacitance	V _{DS} =25V, V _{GS} =0V, F=1MHz	---	425	620	pF
C _{oss}	Output Capacitance		---	26	40	
C _{rss}	Reverse Transfer Capacitance		---	3.2	7	
R _g	Gate resistance	V _{GS} =0V, V _{DS} =0V, F=1MHz	---	1.9	3.8	Ω

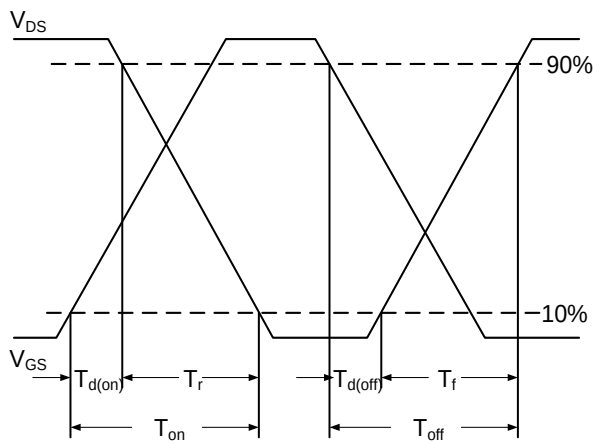
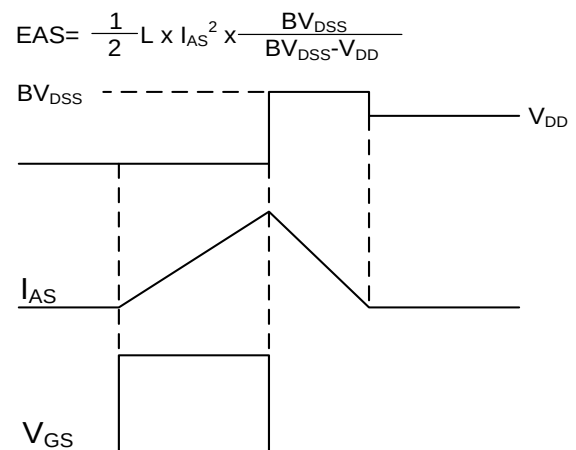
Drain-Source Diode Characteristics and Maximum Ratings

Symbol	Parameter	Conditions	Min.	Typ.	Max.	Unit
I _S	Continuous Source Current	V _G =V _D =0V, Force Current	---	---	2	A
I _{SM}	Pulsed Source Current		---	---	4	A
V _{SD}	Diode Forward Voltage	V _{GS} =0V, I _S =1A, T _J =25°C	---	---	1	V
t _{rr}	Reverse Recovery Time ³	V _{GS} =0V, I _S =1A, dI/dt=100A/μs	---	---	---	ns
Q _{rr}	Reverse Recovery Charge ³	T _J =25°C	---	---	---	nC

Note :

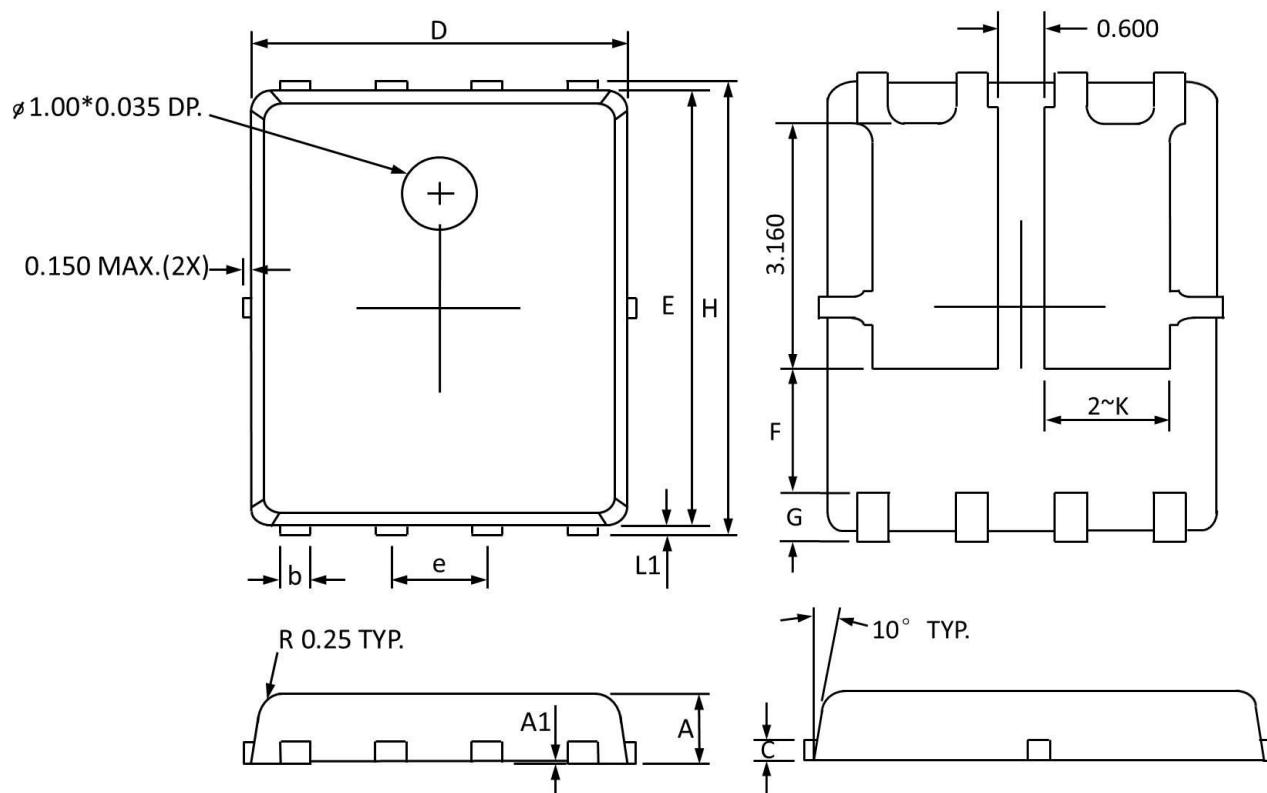
1. Repetitive Rating : Pulsed width limited by maximum junction temperature.
2. V_{DD}=50V, V_{GS}=10V, L=10mH, I_{AS}=3.6A., R_G=25Ω, Starting T_J=25°C.
3. The data tested by pulsed, pulse width ≤ 300us, duty cycle ≤ 2%.
4. Essentially independent of operating temperature.


Fig.1 Continuous Drain Current vs. T_C

Fig.2 Normalized $R_{DS(on)}$ vs. T_J

Fig.3 Normalized V_{th} vs. T_J

Fig.4 Gate Charge Waveform

Fig.5 Normalized Transient Impedance

Fig.6 Maximum Safe Operation Area


Fig.7 Switching Time Waveform

Fig.8 EAS Waveform

$$EAS = \frac{1}{2} L \times I_{AS}^2 \times \frac{BV_{DSS}}{BV_{DSS} - V_{DD}}$$

PPAK5x6 Dual PACKAGE INFORMATION



Symbol	Dimensions In Millimeters		Dimensions In Inches	
	Min	Max	Min	Max
A	0.800	1.000	0.032	0.039
A1	0.000	0.005	0.000	0.000
b	0.350	0.490	0.014	0.019
C	0.254 Ref		0.254 Ref	
D	4.900	5.100	0.193	0.200
E	5.700	5.900	0.225	0.232
e	1.27 BSC		1.27 BSC	
F	1.600 Ref		1.600 Ref	
G	0.600 Ref		0.600 Ref	
H	5.950	6.200	0.235	0.244
L1	0.100	0.180	0.004	0.007
K	1.600 Ref		1.600 Ref	